



Bucket 1 Counter set to 0x003.
Revolution Counter not shown.
Bucket Counter set to 0x0010.
Pulse Width Counter set to 0x0003.
Trigger Counter set to 0x00000010.
Fine Counter Clock set to Variable,
Pulse Width Counter Enable Selector set to Bunch Fill Pattern,
Trigger Counter Clock Selector set to Fine Counter Clock,
Bucket Counter Enable Selector set to Revolution Counter Terminal Count (TC),
Revolution Counter Enable Selector set to Event.

This channel will output a trigger that is 3RF clocks wide for every occurrence of the bunch fill clock. Triggers will start at bucket 17 after X revolutions.
16 triggers will occur and will re-enable after the Trigger Counter reaches terminal count.
For specific Bunch Triggers to occur on every Revolution, Revolution Counter must be 0x0001.
This Channel could be configured to output a Fixed Pulse Width Fine Counter Clock.
FineCounterClock can be phased shifted in increments of 500ps to yield the output triggers.

Timing Diagram #2 - Many output pulses based on Bunch Fill Pattern